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The original idea behind The Art of Hardware Architecture was to link my years of experience as a design architect with the extensive research I have conducted. However, achieving the nal shape of this book would not have been possible without many contributions.

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The hardware architecture is a view of the physical architecture, which represents the hardware components and their interrelationships. The ESS Hardware Block Definition Diagram is shown in Figure 16.47. It aggregates the hardware in a similar way to the ESS Software Block Definition Diagram in Figure 16.41. Sign in to download full-size image

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Art of Hardware Architecture : Design Methods and Techniques for Digital Circuits, Hardcover by Arora, Mohit, ISBN 1461403960, ISBN-13 9781461403968, Brand New, Free shipping in the US The Art of Hardware Architecture highlights the complex issues, tasks and skills involved with digital circuit design.

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There are a lot of books out there that purport to talk about design techniques; but when you get right down to the "nitty-gritty", many of them turn out to be largely theoretical in nature. By comparison, The Art of Hardware Architecture is firmly focused on describing and solving real-world problems using tried-and-true techniques'.

**Art of Hardware Architecture** **----** **Mohit Arora**

In engineering, hardware architecture refers to the identification of a system's physical components and their interrelationships. This description, often called a hardware design model , allows hardware designers to understand how their components fit into a system architecture and provides to software component designers important information needed for software development and integration.

**Hardware architecture** **----** **Wikipedia**

Coverage focuses on real problems rather than theoretical concepts, with an emphasis on design techniques across various aspects of chip-design.The Art of Hardware Architecture (Hardcover)

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An instruction set architecture (ISA) is the interface between the computer's software and hardware and also can be viewed as the programmer's view of the machine. Computers do not understand high-level programming languages such as Java, C++, or most programming languages used. A processor only understands instructions encoded in some numerical fashion, usually as binary numbers.

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